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ISSUE CLASSIFICATION	
Class	Subclass

PATENT NUMBER

U.S. **UTILITY** Patent Application

 O.I.P.E. SCANNED <u>AC³</u> Q.A. <u>3</u>	PATENT DATE
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APPLICATION NO. 09/910753	CONT/PRIOR	CLASS 257 428	SUBCLASS 378 234	ART UNIT 281 2826	EXAMINER Ford
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APPLICANTS

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Steven Franson

TITLE

Structure and method for fabricating configurable transistor devices utilizing the formation of a compliant substrate for materials used to form the same

PTO-2040
12/99[illegible]

<input type="checkbox"/> TERMINAL DISCLAIMER	DRAWINGS		CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner)		NOTICE OF ALLOWANCE MAILED _____	
<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S. Patent. No. _____ _____ _____	_____ (Primary Examiner)		ISSUE FEE Amount Due Date Paid	
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